



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE::MADANAPALLE
(UGC AUTONOMOUS)

www.mits.ac.in

(Affiliated to JNTUA, Anantapuramu & Approved by AICTE, New Delhi)
An ISO 9001: 2008 Certified Institution

ACADEMIC SECTION - NOTIFICATION DATE: 03.12.2015

M.Tech. I Semester I Mid – term Test Schedule December – 2015

M.Tech. I semester (2015 Admitted Batch)

Academic Year – 2015 - 16

TIMINGS: FN: 10:30 AM to 12:30 PM
AN: 02:30 PM to 04:30 PM

DATE / DAY		EPS	SPS	AMS	DECS	VES	CSE
10.12.2015 Thursday	FN	Modern Control Theory	Modern Control Theory	Automation in Manufacturing	Structural Digital Systems Design	Structural Digital Systems Design	Programming Using Python
	AN	Power System Control & Stability	Power Electronic Converters	Advanced Manufacturing Processes	Advanced Computer Networks	Embedded System Concepts	Advanced Data Structures and Algorithms
11.12.2015 Friday	FN	Operation and Control of Power System	Solar PV Technology	Material Technology	Digital Communication Techniques	VLSI Technology	Computer System Design
	AN	Integration Of Renewable Energy Sources	Basis of Solar Cell Devices	Product Design and Development	Advanced Digital Signal Processing	Analog & Digital IC Design	Advance Databases
12.12.2015 Saturday	FN	Analysis of Power Electronic converters	Energy Storage in Solar Power System	Theory of Metal Cutting & Tool Design	Adaptive Signal Processing	Hardware Description Languages	Object Oriented Analysis Design and Design Pattern
	AN	Artificial Intelligence Techniques	Artificial Intelligence Techniques	Advanced Computer Aided Design	Mobile Networks	MEMS	Grid computing

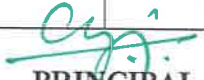
Note: All HODs are requested to circulate among the faculty in the department.

Copy to:

1. All HODs : EEE ME ECE CSE HUM

EC To:

2. SAO(G) SAO (A&P) S.P.O AO(Systems) SWO Dean(R&D) Dean(Academic) TC Librarian SO TI Dy. Warden
3. Copy to HOD's to be read in M.Tech I Semester Class Rooms 4. Department & Academic Section Notice Boards


PRINCIPAL
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 Madanapalle Institute of Technology & Science
 P.O. No: 14, Kadiri Road, Angallu
MADANAPALLE - 517 323, A.P.